

**Notice of References Cited**

Application/Control No.

09/748,098

Applicant(s)/Patent Under  
Reexamination  
KELSEY ET AL.

Examiner

David J. Huisman

Art Unit

2183

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**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
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**FOREIGN PATENT DOCUMENTS**

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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Shaaban, Clock Signals & Synchronous Sequential Circuits, January 9, 2002, pg.5
	V	Wikipedia - The Free Encyclopedia (entry: Clock Cycle), 2007, pp.1-2
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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